

Page	Location	Change
59	Line 23	INCO → INC0
79	Fig.6-7	Change handling of INTCON at beginning and end of LoopTime subroutine to be the same as on page 62
88	Fig. 7-6c	Change PORTE,0 → PORTE,RE0 Change PORTE,1 → PORTE,RE1
89	Fig. 7-8b	same
91	Fig. 7-10	same
92	Fig. 7-11	same
98	Fig. 7-17	same (in both Bargraph subroutine and SendByte subroutine)
102	Fig. 7-19	same (in InitLCD subroutine)
103	Fig. 7-19	same (in DisplayC subroutine and DisplayV subroutine)
121	Line 2	2 byte → two-byte
122	Line 37	or 5 microseconds. → or 5 milliseconds.
135	Fig. 10-1	V <sub>DC</sub> → V <sub>DD</sub>
172	Fig. 13-2	Last caption should be named (d), not (a)
173		The description of the CyclesToMicrosec should be much simpler, starting with: The CyclesToMicrosec subroutine of Figure 13-4 carries out the conversion Microseconds = (Cycles x 2)/5 (the rest of the page should reflect this much simpler scheme)
174	Fig. 13-4, line 4	; Microseconds = 0.4 Cycles = (Cycles x 2)/5
174	Fig. 13-4	CyclesToMicrosec bcf STATUS,C ;Multiply by 2 rlcf AARGB2,F rlcf AARGB1,F rlcf AARGB0,F MOVLf 5,BARGB0 ;Divide by 5 call FXD2408U return
182	Fig. 13-12	In labeling of IPR2 register, CPP2IP → CCP2IP
197	Line 31	FPSUBS.Inc → FPSUBS.inc
200	Line 35	;Form TBLPTR = PowTen + 4(TMP0) → ;Form TBLPTR = PowTen + 4(TMP0-1)
200	Line 36	Delete MOVLf upper PowTen, TBLPTRU
200	Line 58	Change call INT3224 ; and convert it to a three-byte integer return to call INT3232 ; and convert it to a four-byte signed integer ; and from this to a three-byte unsigned integer movff AARGB1,AARGB0 ; (Note that the largest three-byte signed integer is movff AARGB2,AARGB1 ; 0x7ffff = 8,388,607 and that we need to reach values movff AARGB3,AARGB2 ; all the way up to 9,999,999) return
235	Fig. 16-10	0010 toggel RC2/CCP1 pin on compare → 0010 toggle RC2/CCP1 pin on compare
237	Fig. 16-11	Move btg CCP1CON,0 ;Toggle control bit from the first line of the IF... ENDIF_ construct to just before the ENDIF_ construct
246	Line 22	Remove the words with STOP condition
256	Fig. 17-11b	Change caption: 7-bit I <sup>2</sup> c address → 7-bit I <sup>2</sup> C address
259	Line 1	CH17 for GT QwikPIC board → CH17 for QwikFlash board
260	Fig. 17-15	Switch the last three lines of the Initial subroutine to rcall InitLCD ;Initialize LCD rcall InitI2C ;Initialize I2C bus and devices return
263	Line 10	return → goto \$ ;Halt
315	Line 2	Change bsf RCON,NOT_POR ;Set bit to distinguish from other resets to setf RCON ;Initialize all flag bits at power on
348	Line 5	Change from UNTIL_ PIR1,TX1F ;Wait, if necessary, on previous transfer to UNTIL_ PIR1,TXIF == 1 ;Wait, if necessary, on previous transfer
401	Line 24	mpasm p1 → mpasmwin p1
401	Line 25	mpasm.exe → mpasmwin.exe